

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Carles Borrego Bel, et al.

Serial No.: 10/709,677

Filed: May 21, 2004

For: SYSTEM AND METHOD FOR PROTECTING AGAINST  
SHORT CIRCUITS IN ELECTRIC POWER DISTRIBUTION  
ARCHITECTURES WITH TWO VOLTAGE LEVELS

Attorney Docket No.: LEAR 8153ES PUSA

Group Art Unit: 2836

Examiner: Adi Amrany

**NOTICE OF APPEAL UNDER 37 C.F.R. § 41.31**

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Commissioner for Patents  
U.S. Patent & Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision of the Examiner dated February 22, 2007 in the above-identified patent application.

Payment of the appeal fee of \$500.00 as applicable under the provisions of 37 C.F.R. § 41.20(b)(1) is being filed electronically herewith. Please charge any additional fee or credit any overpayment in connection with this filing to our Deposit Account No. 02-3978.

Respectfully submitted,

**Carles Borrego Bel, et al.**

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Date: May 18, 2007  
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